



AI HARDWARE SUMMIT

September 17 – 18, 2019

THE INDUSTRY EVENT FOR THE AI CHIP ECOSYSTEM

Computer History Museum, Mountain View, CA
www.aihardwaresummit.com

Key announcements
defining the next
12 months of the AI
Hardware industry, from
the event that brought
you [Habana Labs](#).



500+ CEOs, CTOs,
Architects & Engineers
forms the only true
leadership forum for AI
Hardware.



Luminary Keynote from:
 John L. Hennessy
Chairman
Alphabet Inc.



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Platinum Partners



Headline Media Partner:



✓ **DEFINING THE NEXT 12 MONTHS OF THE AI HARDWARE INDUSTRY.**

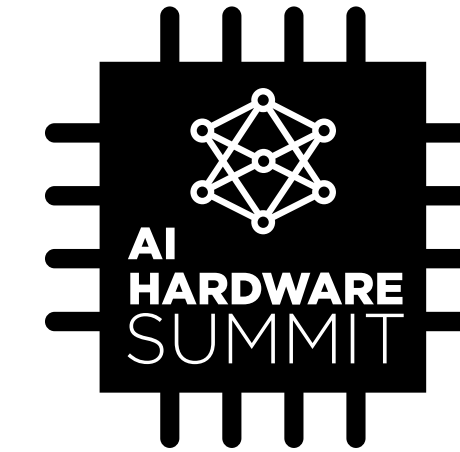
Habana Labs launched their company and processor at AI Hardware Summit 2018, and Graphcore and ThinCI released details of silicon. This year, we guarantee more announcements that will shape the next 12 months of the industry.

✓ **500+ CEOS, CTOS, ARCHITECTS & ENGINEERS FORMS THE ONLY TRUE LEADERSHIP FORUM FOR AI HARDWARE.**

Our world class network includes C-level from 80% of AI chip start-ups worldwide, and BU heads from semiconductor companies, hyperscalers & OEMs.

✓ **OPTIMIZE AI ACROSS THE WHOLE STACK, FROM SILICON TO ALGORITHMS, ENABLING GROUNDBREAKING PERFORMANCE.**

AI hardware customer perspectives from Microsoft, Facebook, Google, Alibaba, Dell, IBM, Panasonic, Medallia, Uber, Xiaopeng Motors, ArgoAI and Nuro.



This year, we are honored to have Dr. John L. Hennessy, Chairman of Alphabet, former President of Stanford University and Turing Award laureate, open the AI Hardware Summit.

Following a huge success last year, and a sold out event, I'm delighted to be returning to the Computer History Museum on September 17 - 18 and want to share our mission statement for this year's summit:

- 1 To bring together the critical mass of the global industry to promote innovation and adoption of silicon & systems for AI processing.
- 2 To serve as the venue where the roadmap of emerging AI hardware technology is discussed and updated each year.
- 3 To connect silicon & systems vendors & hardware innovators to customers, partners & investors.

This year's summit will unveil the AI chip companies who have products ready for customers, and will highlight case studies of where AI hardware is already impacting research and business. Whether building hardware, or buying it, this is the event to attend if accelerating AI is your focus.

I hope to see you there!

Ed Nelson,
Conference Director,
AI Hardware Summit

WHY ATTEND AI HARDWARE SUMMIT

Luminary Keynotes:

Unique perspectives from industry luminaries:

John L. Hennessy
Chairman
[Alphabet, Inc.](#)

Lip-Bu Tan
CEO, Cadence Design
Systems & President,
[Walden International](#)

Innovations and Optimizations of Silicon & Systems for AI Training & Inference:

Presentations from C-level executives from AI chip start ups, semiconductor companies and systems OEMs.

Covered by:



Training & Inference at Hyperscale & AI Accelerators in the Data Center Hardware Environment:

Deployment & maintenance of AI infrastructure in data centers, hardware requirements for training & inference at scale.

Covered by:



Inference in Client & Edge Computing:

Deployment of AI accelerators in cameras, consumer electronics, autonomous vehicles etc.

Covered by:



Beyond Compute:

AI's Impact on Memory, Storage & Networking: Innovations in HBM, on-chip memory and NVM, I/O bottlenecks, data transfer & high-speed interconnects.

Covered by:



The Impact of Future ML Models on Hardware Design:

Machine Learning co-design, robustness & reprogrammability, model standardization & interoperability.

Covered by:



AI Chip Design & Commercialization:

Design, testing & manufacturing, form factors & routes-to-market.

Covered by:

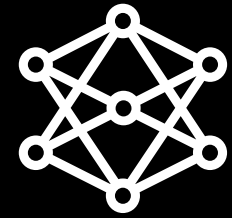


Financial & Industrial Analysis

Market growth and maturity, VC investment trends & dynamics, the commoditization of the inference market, benchmarking & metrics.

Covered by:





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OPENING LUMINARY KEYNOTE

JOHN L. HENNESSY

TUE | 17 SEPT | 9:00 AM

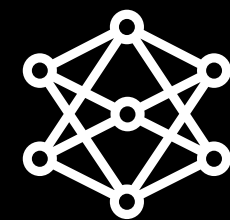
The End of Moore's Law, CPUs (as we know them), and the Rise of Domain Specific Architectures

John L. Hennessy
Chairman
Alphabet Inc

John L. Hennessy is the Chairman of Alphabet Inc., Board Member of Cisco Systems, the former President of Stanford University, a co-founder of MIPS Computer Systems & Atheros, and a recent Turing Award laureate for his pioneering development of RISC architecture, alongside David Patterson.

Dr. Hennessy was awarded the IEEE Medal of Honor in 2012 and was appointed a Fellow of the Computer History Museum in 2007, among a multitude of accolades for his leadership in the field of computer science, architecture & engineering.

In 2016, he co-founded the Knight-Hennessy Scholars program, which has a \$750 million endowment to fully fund graduate students at Stanford for up to three years.



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INVESTMENT LUMINARY KEYNOTE

LIP-BU TAN

WED | 18 SEPT | 1:45 PM

Semiconductor Innovations for AI

Lip-Bu Tan

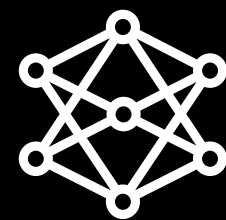
CEO

Cadence Design Systems

Chairman

Walden International

Mr. Lip-Bu Tan is a Co-Founder, Partner, Managing Director, and Chairman of Walden International. Mr. Tan serves as the Chairman on the Board of SambaNova Systems, Inc., and has been the Chief Executive Officer at Cadence Design Systems Inc. since January 2009, serving as its President from January 2009 to November 2017. Lip-Bu focuses on semiconductor/components, cloud/big data, artificial intelligence and machine learning. Lip-Bu holds a B.S. in Physics from Nanyang University in Singapore, a M.S. in Nuclear Engineering from Massachusetts Institute of Technology, and a M.B.A. from the University of San Francisco.



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FEATURED KEYNOTE

NAVEEN RAO

TUE

17 SEPT

5:50 PM

The Next Frontier: Three Technologies That Will Transform AI Hardware

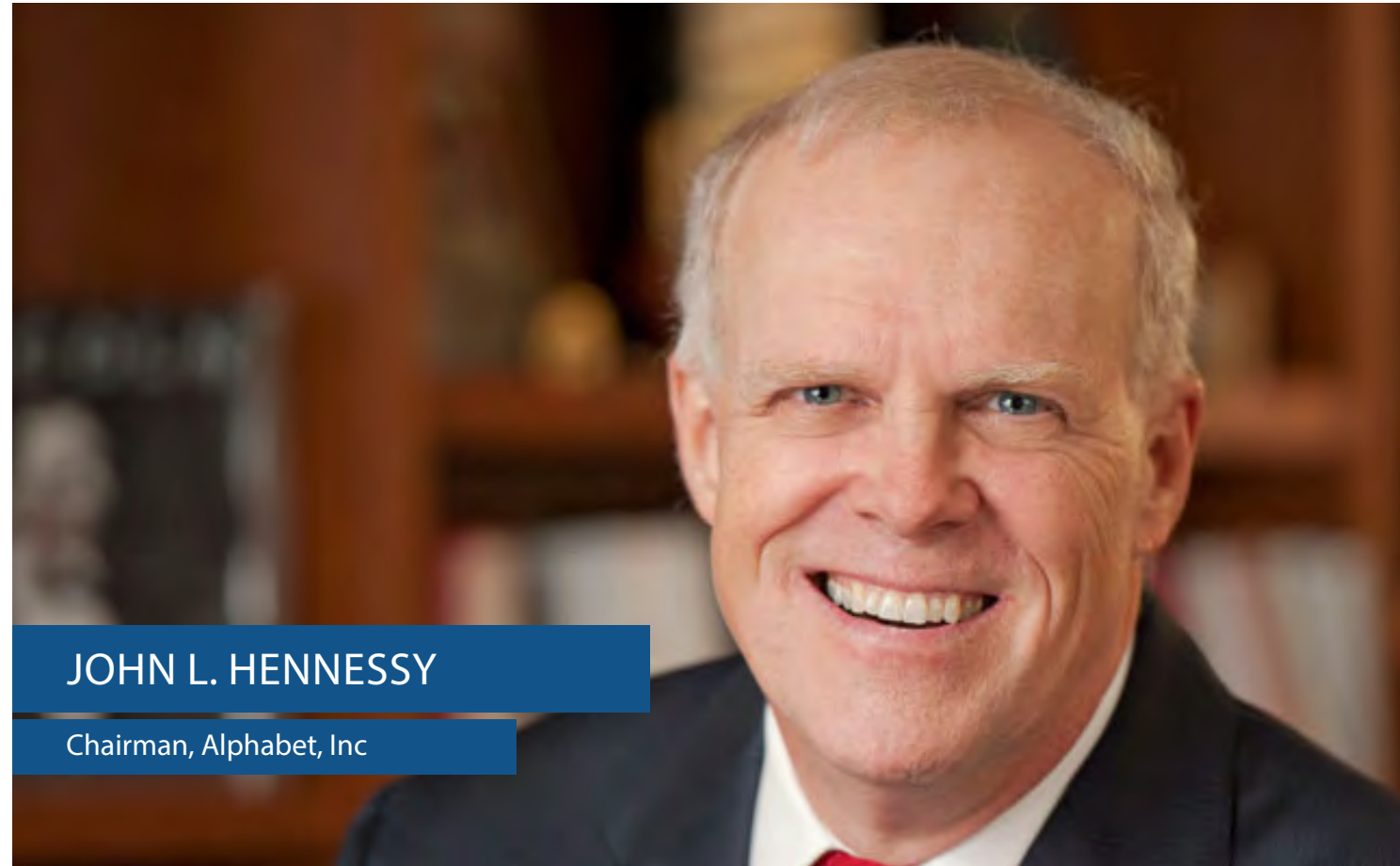
Naveen Rao
CVP & GM, Artificial Intelligence
Products Group
Intel

Naveen Rao is corporate vice president and general manager of the Artificial Intelligence Products Group at Intel Corporation.

Trained as both a computer architect and neuroscientist, Dr. Rao joined Intel in 2016 with the acquisition of Nervana Systems. As chief executive officer and co-founder of Nervana, he led the company to become a recognized leader in the deep learning field. Before founding Nervana in 2014, Rao was a neuromorphic machines researcher at Qualcomm Inc., where he focused on neural computation and learning in artificial systems. Rao's earlier career included engineering roles at Kealia Inc., CALY Networks and Sun Microsystems Inc.

Rao earned a bachelor's degree in electrical engineering and computer science from Duke University, then spent a decade as a computer architect before going on to earn a Ph.D. in computational neuroscience from Brown University

MEET OUR DISTINGUISHED SPEAKERS



JOHN L. HENNESSY

Chairman, Alphabet, Inc



NAVEEN RAO

VP & GM, Artificial, Intelligence Products Group, Intel



LIP BU-TAN

CEO, Cadence Design Systems

Chairman, Walden International



Ian Buck

VP / General Manager,
Tesla Data Center
Business
Nvidia



Andrew Feldman

Co-Founder & CEO
Cerebras Systems



Chris Nicol

Co-Founder & CTO
Wave Computing



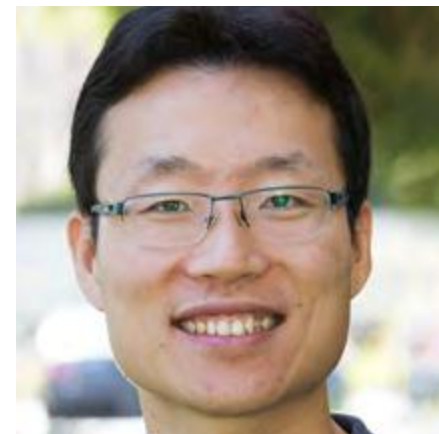
Dr. Kai Yu

Founder & CEO
Horizon Robotics Inc.



Victoria Rege

Head of Strategic
Partnerships
Graphcore



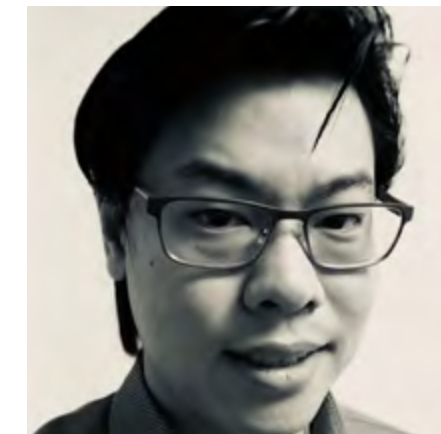
Cheng Wang

Co-Founder & SVP,
Architecture &
Engineering
Flex Logix



Jonathan Ross

Co-Founder & CEO
Groq



Eric Chung

Principal Researcher &
Manager
Microsoft



Mikhail Smeliyanskiy

Technical Lead &
Manager, AI System
Co-Design Group
Facebook



Kunle Olukotun

Co-founder and Chief
Technologist
SambaNova Systems

MEET OUR DISTINGUISHED SPEAKERS CONTINUED



Mike B. Henry
Founder and CEO
Mythic



Jimmy Pike
SVP & Senior Fellow,
Servers and Infrastructure
Systems
Dell MC



Brett Simpson
Partner and Co-Founder
Arete Research



Gregor Stewart
VP, Data Science
Medallia



Sumit Gupta
VP, AI, Machine
Learning, & HPC
IBM



Junli Gu
Vice President of
Autonomous Driving
Xiaopeng Motors



Karl Freund
Senior Analyst, Machine
Learning & HPC
Moor Insights & Strategy



Hari Kannan
Technical Director of
Engineering
Pure Storage



Yichen Shen
Co-Founder & Principal
Lightelligence



Thomas Andersen
Head of Machine
Learning and AI Design
Group
Synopsys



Justin Butler
Partner
Eclipse Ventures



Cliff Young
Software Engineer
Google Brain



Márton Fehér
SVP, Hardware
AIMotive



Rashmi Gopinath
Partner
M12
(Microsoft Ventures)



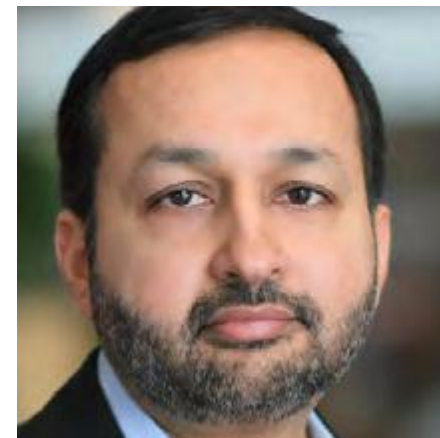
Samal Dalal
Senior Manager (TLM),
Architecture & Design
Uber



**Michael P. Stewart,
Ph.D.**
Investment Director
Applied Ventures



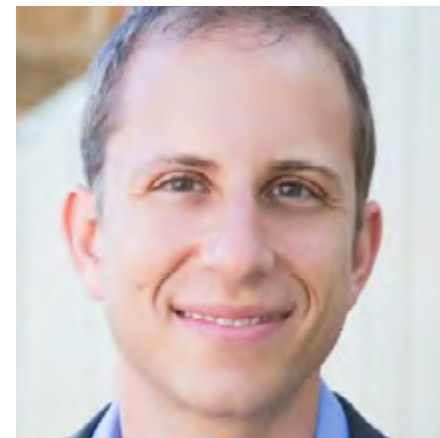
Yvonne Lutsch
Investment Principal
Bosch Ventures



Ziad Asghar
Snapdragon Roadmap
Planning and AI, XR &
Competitive Strategy
Qualcomm



Albert Meixner
Head of Software
Infrastructure
Nuro



David Kanter
Inference Co-Chair
ML Perf

MEET OUR DISTINGUISHED SPEAKERS CONTINUED



Lingjie Xu
Director, Applied AI
Architecture
Alibaba Inc.



Gordon Hirsch-Wilson
Co-Founder & CEO
RAIN Neuromorphics



Ching Hu
Staff System Architect
Argo AI



Christopher Nguyen
President & CEO
Arimo (Panasonic)



Patrick Soheili
VP Corporate
Development & Head of
AI Strategy
eSilicon



Dr. Prith Banerjee
Chief Technical Officer
ANSYS



Eitan Medina
Chief Business Officer
Habana Labs



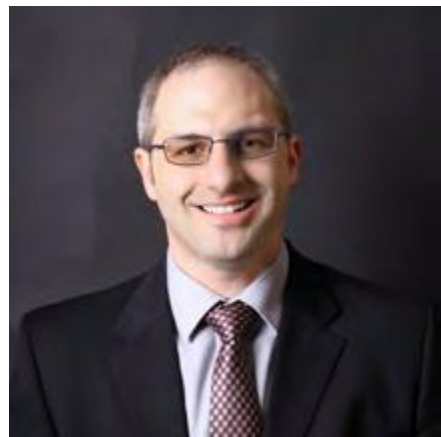
Eric Baissus
CEO
Kalray



Ingolf Held
CEO
GrAI Matter Labs



Steven Woo
Fellow & Distinguished
Inventor
Rambus



Bryan Bowyer
Director of Engineering
Mentor, A Siemens
Business



Whitney Zhao
Hardware Engineer
Facebook



Justin Potuznik
Senior Principal Engineer
& Architecture Lead
Optum Technology



Robert Gendron
CVP, Product
Management & Technical
Resources
Vicor

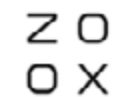


Chandu Thekkath
Distinguished Engineer
Microsoft



Moe Tanabian
CVP, Product GM,
Intelligent Devices
Microsoft

WHO ATTENDS AI HARDWARE SUMMIT



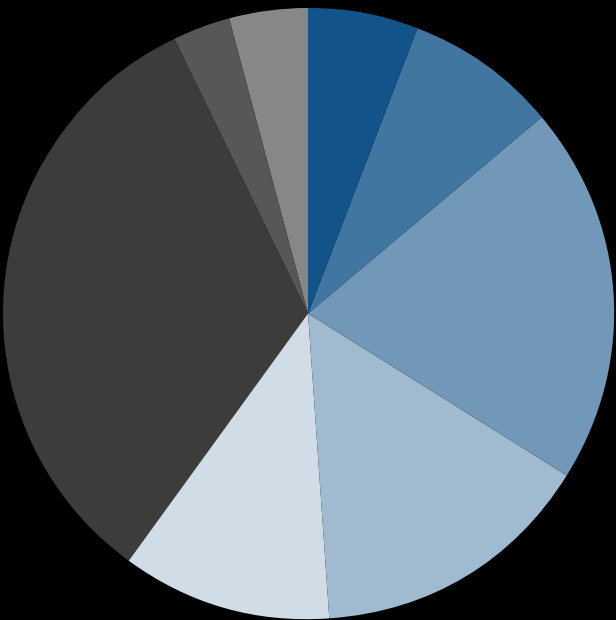


AUDIENCE BREAKDOWN

Must Attend Category

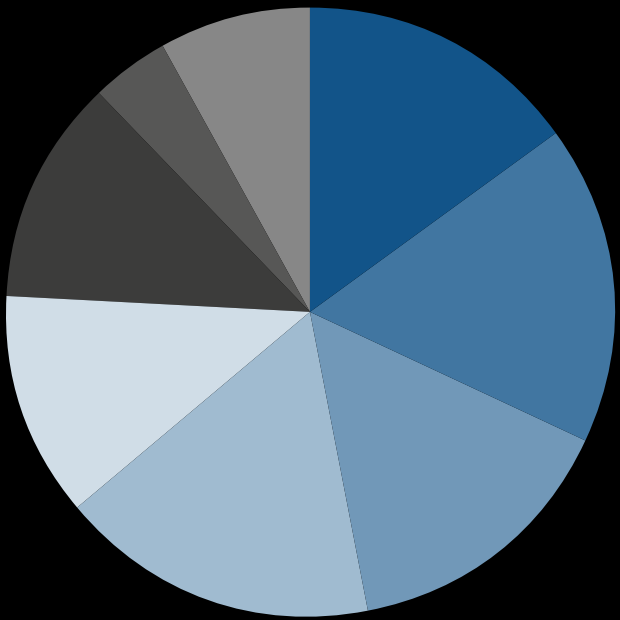
- AI Chip Start Ups
- Large Semiconductor
- EDA & IP Vendors
- Hyperscalers & Data Centers
- Device Manufacturers
- Server Vendors
- Autonomous Vehicle Companies
- Robotics
- Government Defence & Aerospace
- Enterprise
- Pharma
- Retail
- eCommerce
- Media
- AI Research Institutions
- Investors
- Press & Analysts

Job Function

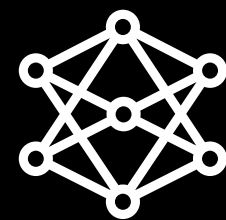


- 6% Analyst
- 8% Architect
- 20% Engineer
- 15% Senior Mgmt
- 11% BD/Marketing
- 33% Strategy
- 3% Media
- 4% Investor

Job Seniority



- 15% C-Level/Founder/Senior Exec
- 17% VP/Head of Dept
- 15% Sr. Director/Director
- 17% Sr. Manager/Manager
- 12% Technical Lead
- 12% Non-management
- 4% Media/Market Analysts
- 8% Investors/Investment Analysts



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DAY ONE | TUE | 17 SEPT

AI Acceleration in Server & Cloud Computing

8:00 AM

Breakfast Networking Session

8:55 AM

Chair's Opening Remarks

9:00 AM

OPENING LUMINARY KEYNOTE: The End of Moore's Law, CPUs (as we know them), and the Rise of Domain Specific Architectures.



John Hennessy
Chairman
[Alphabet](#)

9:30 AM

INDUSTRY ANALYSIS: The Expanding Field of AI Accelerators



Karl Freund
Senior Analyst, HPC & Machine Learning
[Moor Insights & Strategy](#)

9:55 AM

Designing Computer Systems for Software 2.0



Kunle Olukotun
Co-Founder & Chief Technologist
[SambaNova Systems](#)

10:20 AM

Groq: A New Class of Compute



Jonathan Ross
Co-Founder & CEO
[Groq](#)

10:45 AM

Networking Break

11:25 AM

Cerebras Systems: AI at SuperCompute Scale



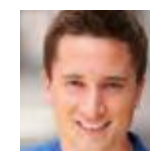
Andrew Feldman
Co-Founder & CEO
[Cerebras Systems](#)

11:45 AM

Programmability & Configurability: Future Proofing AI Hardware

12:05 PM

Winning at Inference - More than ResNet-50 Benchmarking



Mike Henry
Co-Founder & CEO
[Mythic](#)

12:25 PM

Vicor: The Need for New AI Processor Power Delivery



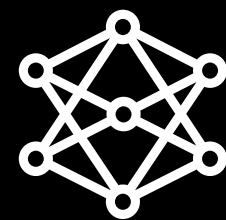
Robert Gendron
CVP, Product Management & Technical Resources
[Vicor](#)

12:40 PM

Lunch

"I think this conference is turning out to be great, in the sense that the density of companies attending it is phenomenal. We've have a world premiere, in Habana Labs, and all of the top notch companies are here - not fully disclosing what they are doing, but at least you get some insight from the CEOs and CTOs. It's been great!"

Marc Tremblay, Distinguished Engineer, [Microsoft](#)



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DAY ONE | TUE | 17 SEPT

AI Acceleration in Server & Cloud Computing

1:25 PM WORKSHOP

A Standards-based Approach to Scaling AI
Datacenter Capacity and Performance

Sponsored by 

2:10 PM

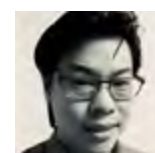
The Need for New Hardware for Artificial
Intelligence: Use Case Overview



Victoria Rege
Head of Strategic Partnerships
[Graphcore](#)

2:30 PM

Architecting Systems for AI at Cloud Scale
and Edge: A Whole Stack Challenge



Eric Chung
Principal Researcher & Manager
[Microsoft](#)

2:55 PM

AI System Co-Design: How to Balance
Performance & Flexibility



Mikhail Smelyanskiy
Director, AI System Co-Design
[Facebook](#)

3:20 PM

CASE STUDY: Scaling Machine Learning
for Text Understanding



Sumit Gupta
VP, AI, ML & HPC
[IBM](#)



Gregor Stewart
VP, Data Science
[Medallia](#)

3:45 PM

CASE STUDY: Dell EMC Systems & Optum
Technology Accelerating AI in Healthcare



Jimmy Pike
SVP & Senior Fellow
[Dell EMC](#)



Justin Potuznik
Senior Principal Engineer & Architecture
Lead
[Optum Technology](#)

4:10 PM

Networking Break

4:50 PM

Counterintuitive Intelligence: Doing More
to Cost Less. A TCO Story



Patrick Soheili
VP, Corporate Development & Head of AI
Strategy
[eSilicon](#)

5:10 PM

PANEL: Developments and Impacts of
Emerging Industry Standards on the AI
Datacenter



Samar Dalal
Senior Manager, Architecture & Design
[Uber](#)



Cliff Young
Software Engineer
[Google Brain](#)



Eitan Medina
CBO
[Habana Labs](#)

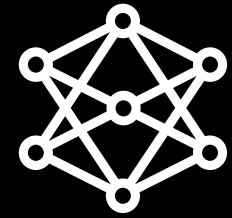


Lingjie Xu
Director Applied AI Architecture
[Alibaba Inc.](#)



Whitney Zhao
Hardware Engineer
[Facebook](#)

As demand for greater productivity and efficiency in scaling AI performance in the datacenter and cloud grows, open standards for hardware such as the OCP-OAM, networking technologies like RoCE and open-source software solutions, are emerging from industry alliances and companies. This panel consisting of leaders across the AI datacenter ecosystem will discuss key trends occurring with these open standards and the potential impacts open standards will likely have on the way systems will be built for serving AI in the datacenter.



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DAY ONE | TUE | 17 SEPT

AI Acceleration in Server & Cloud Computing

5:35 PM

PANEL: Beyond Compute: AI's Impact on
Memory, Storage & Networking



Steven Woo
Fellow & Distinguished Inventor
[Rambus](#)



Hari Kannan
Technical Director of Engineering
[Pure Storage](#)

Compute is only one part of the AI processing problem, presenting opportunity for innovation in memory, storage and networking. Memory experts will weigh innovations in high-bandwidth and on-chip memory, plus forthcoming opportunities in non-volatile memory. Storage innovators will discuss the potential of AI-optimized storage systems and confront I/O bottlenecks in AI processing, while leaders in Networking will cover overcoming data transfer challenges and opportunities for high-speed interconnects.

6:00 PM

CLOSING KEYNOTE: The Next
Frontier: Three Technologies That Will
Transform AI Hardware

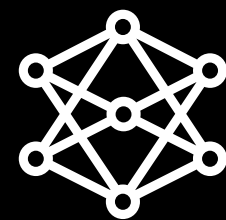


Naveen Rao
VP, AI Products Group
[Intel](#)

6:30 PM

Networking Drinks, hosted by Intel





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DAY TWO | WED | 18 SEPT

AI Acceleration On-Device & Industry Trends & Dynamics

8:00 AM

Breakfast Networking Session

8:25 AM

Chair's Opening Remarks

8:30 AM

OPENING KEYNOTE: Enabling
On-Device Intelligence



Thomas Andersen
Head of Machine Learning &
AI Design Group
[Synopsys](#)

9:00 AM

From TOPS to Throughput: How to Get
the Most Throughput From the Least
Hardware



Cheng Wang
Co-Founder & SVP, Architecture &
Engineering
[Flex Logix](#)

9:25 AM

China's First Commercial AI Vision
Processor: A Full-Stack Solution for AI



Kai Yu
Co-Founder & CEO
[Horizon Robotics](#)

9:50 AM

Networking Break

10:30 AM

Distributed Intelligence, from Edge to
Cloud



Ziad Asghar
VP, Product Management, Snapdragon
Roadmap Planning & AI, XR and
Competitive Strategy
[Qualcomm](#)

10:55 AM

Accelerating AI at the Speed of Light



Yichen Shen
Co-Founder & CEO
[Lightelligence](#)

11:20 AM

Building Bigger Brains: Using
Neuromorphics to Scale Up AI



Gordon Hirsch-Wilson
Co-Founder & CEO
[RAIN Neuromorphics](#)

11:40 AM

NeuronFlow – Real World Solutions for
Live AI



Ingolf Held
CEO
[GrAI Matter Labs](#)

12:00 PM

Lunch

12:30 PM WORKSHOP

Accelerating Adoption of Edge AI

Sponsored by



1:30 PM

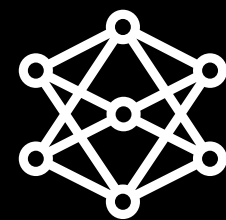
LUMINARY KEYNOTE: Investment
Semiconductor Innovations for AI



Lip-Bu Tan
CEO [Cadence Design Systems](#)
Chairman [Waldern International](#)

"It's a great mix of big
companies, start-ups,
investors, journalists,
I recognized a lot of
faces here, I mean this
is probably the most
concentrated group
of relevant people I've
been to, it's definitely
worth attending."

Mike B. Henry, Founder and
CEO, [Mythic](#)



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DAY TWO WED 18 SEPT

AI Acceleration On-Device & Industry Trends & Dynamics

2:00 PM

PANEL: Industry Trends & Dynamics in AI Hardware: 2018-2020



Rashmi Gopinath
Partner
[M12](#)



Yvonne Lutsch
Investment Principal
[Bosch Ventures](#)



Michael P. Stewart
Investment Director
[Applied Ventures](#)



Justin Butler
Partner
[Eclipse Ventures](#)



Brett Simpson
Co-Founder & Partner
[Arete Research](#)

As the AI hardware market matures, routes-to-market, form factors and software support are issues that are occupying the minds of both vendors and buyers of new hardware, as performance metrics abound. This panel will offer an investor perspective on the forces driving the market and insight into what may cause inflection points in the industry. The panel will take a look at how the industry has changed from last year, and how it might look by the end of next year.

2:30 PM

FIRESIDE CHAT: Challenges and Design of ML Benchmarks:



David Kanter
Inference Co-Chair
[ML Perf](#)



Lingjie Xu
Director, Applied AI Architecture
[Alibaba](#)

2:55 PM

Lowest Power at the Edge: Keeping up with New Architectures Using HLS



Bryan Bowyer
Director of Engineering
[Mentor, A Siemens Business](#)

3:10 PM

Consumer & Enterprise AI at Panasonic: The Hardware-Software Transformation



Christopher Nguyen
President & CEO
[Arimo, Panasonic](#)

3:35 PM

Networking Break

4:15 PM

An Apples to Apples Comparison of AI Hardware for Autonomous Vehicles



Junli Gu
VP, Autonomous Driving
[Xiaopeng Motors](#)

4:40 PM

Doing Just One Task Really Well: Optimizing Hardware for Automotive AI



Márton Fehér
SVP, Hardware Engineering
[AIMotive](#)

5:00 PM

PANEL: Architecting AI Systems for Autonomous Vehicles: The Interplay of Hardware & Software



Ching Hu
Staff System Architect
[Argo](#)



Albert Meixner
Head of Software Infrastructure
[Nuro](#)



Dr. Prith Banerjee
Chief Technical Officer
[ANSYS](#)



Eric Baissus
CEO
[Kalray](#)

Nowhere else is the optimization and symbiosis of software and hardware more complex, and more critical, than in autonomous vehicles. From robots, to passenger-carrying vehicles, systems design and architecture must be meticulous, with rigorous testing, simulation and optimization at the component and system level, to ensure reliability, performance and safety.

Our panellists will discuss what hardware vendors need to think about to design products for autonomous vehicles, and how important flexibility and software support is.

5:25 PM

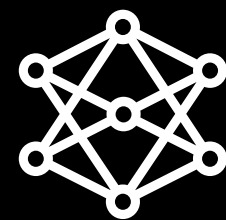
CLOSING KEYNOTE



Ian Buck
VP & GM, Tesla Data Center Business
[NVIDIA](#)

5:55 PM

Chair's Closing Remarks



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DEEP-DIVE WORKSHOPS

DAY ONE
TUES 17 SEPT
1:25PM

A Standards-based Approach to Scaling AI Datacenter Capacity and Performance



Eitan Medina
CBO
Habana Labs

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This workshop will provide a maximum of 35 attendees with a deep-dive on the foundational architecture and technologies that Habana Labs employs in its recently announced GAUDI AI Training Processor to efficiently scale AI Datacenter capacity and performance. Eitan will provide specifics on how systems can be built from a small-scale server to medium and large-scale systems to address both data parallel and model parallel computing requirements.

Attendees will be shown a private demonstration of the company's AI Training Processor performance and see how it scales versus legacy GPU solutions.

DAY TWO
WED 18 SEPT
12:30PM

Accelerating Adoption of Edge AI



Moe Tanabian
GM, Intelligent Devices
Microsoft



Chandu Thekkath
Distinguished Engineer
Microsoft

Sponsored by 

AI on edge devices is entering the “peak of inflated expectations.” Despite the potential for unprecedented gains in many areas, the complexity and bespoke nature of edge AI implementations means that this technology has the potential for a long stay in the “trough of disillusionment”, with only the most mature companies, with the most to invest, reaping the full benefits.

Based on our experience using Azure AI perception capabilities to build HoloLens 2 and Azure Kinect DK, and partnering with many early adopted of edge AI across industries and scenarios, Microsoft is investigating strategies, technologies and tools to accelerate the adoption/return on investment for developing and deploying AI solutions on edge devices.

In this invite only workshop, Moe Tanabian, GM of Azure Devices and Chandu Thekkath, Distinguished Engineer of AI Platforms, will discuss where Microsoft see the most significant opportunities, how the HW ecosystem and developer community have a role to play in accelerating adoption. Attendees will get the opportunity to provide feedback on early stage product strategy from Microsoft.

Attending this workshop requires signing an NDA prior the conference start.

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Expires: July 19th, 2019

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HEAR WHAT 2018 ATTENDEES HAVE TO SAY...



"The reason I decided to attend is because this is really a one of a kind event...this was a great opportunity and learn more about what's happening in the industry and also talk to everyone about our point of view...this is a great opportunity to meet everyone from the Intels and NVIDIAs of the world, to the next generation of start-ups like Cerebras and Graphcore and seeing who are the next generation of these companies that we should be looking out for."

Rashmi Gopinath
Partner
M12 (Microsoft
Ventures)



"I came to get exposed to a different set of people from the ones I usually meet, I know academic people and I know Googlers but I don't know the business people and I don't know nearly as many start up people and so this is a different slice of the community, that I only sometimes interact with, where my work is clearly relevant but I'm usually a few steps removed."

Cliff Young
Software Engineer
Google Brain



"If you're developing a chip, you need to be at this conference...so for no other reason just competitive market research, you should definitely be at this event."

Karl Freund
Senior Analyst,
Machine Learning & HPC
Moor Insights & Strategy



"We are always looking to work with really great engineering teams with technical innovations in areas we think are commercially fertile. I know the AI Hardware Summit has really strong people so I was really glad to speak here."

Sarah Guo
Partner
Greylock Partners



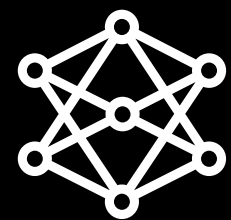
"It's easy to say conferences are dead but the truth is you bump into people you haven't spoken to in a while...this is how relationships get made."

Andrew Feldman
Co-Founder & CEO
Cerebras

2018 HIGHLIGHTS

Click to watch highlights from last year's sold out AI Hardware Summit





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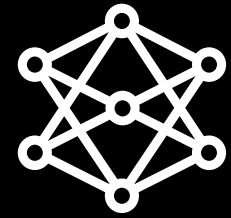


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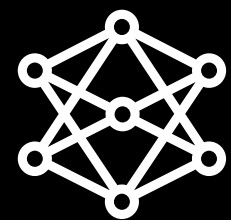
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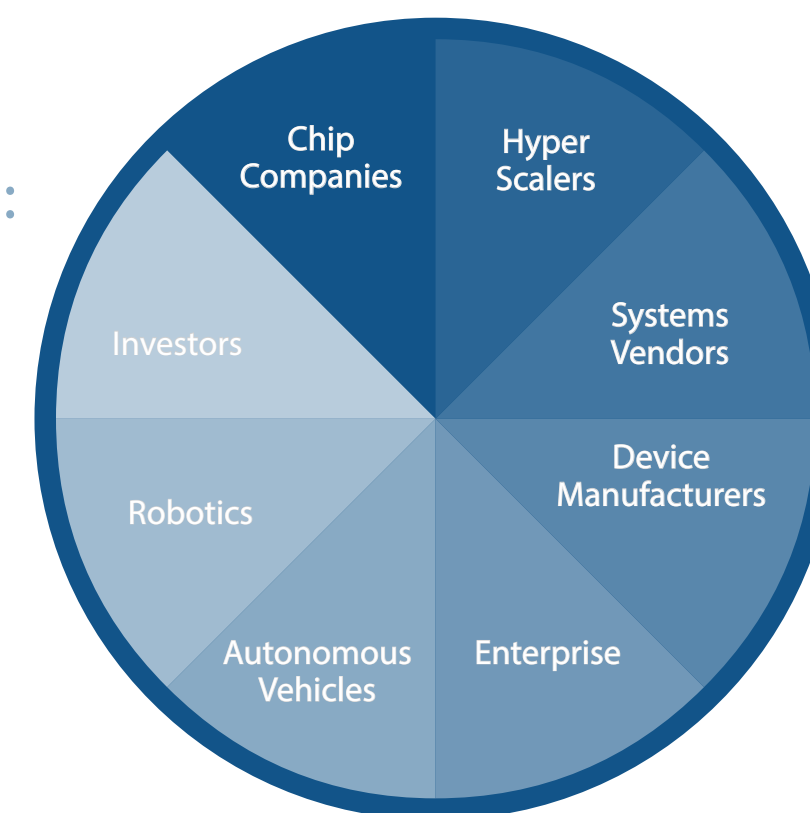
PARTNERING OPPORTUNITIES

As the only event dedicated to AI Hardware, this summit offers attendees, sponsors, and event partners access to representatives of the entire AI hardware ecosystem, delivering unique business development opportunities. Contact our team to position your organization as a leader in this emerging space, and leverage bespoke business development and marketing packages.

We have a range of sponsorship packages available but are we are dedicated to understanding your objectives. Talk to us to discuss what you want to achieve and we can build a bespoke package to meet your needs.

Sponsor Level	Thought Leadership (keynote, presentation, workshop opportunities)	Brand awareness	Networking (exhibitor booth & networking facilitation options)	Branding level
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Gold	✓	✓	✓	Superior
Silver		✓	✓	Superior
Event Partner			✓	Standard

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key opinion
leaders from:

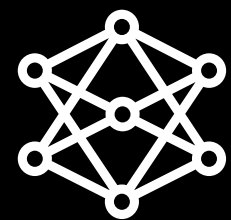


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For further details or to create a tailored
solution, please contact:

Ben Edwards, Commercial Director
Partner@kisacoresearch.com,
+ 44 207 897 6805

*Range of options available



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Package	Early bird (ends July 19th)	Standard rate
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Financial Services & Investors	\$2,199	\$2,499

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We hope to see you there.



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